

Notice of References Cited	Application/Control No. 09/592,106	Applicant(s)/Patent Under Reexamination MELVIN ET AL.	
	Examiner Larry D Donaghue	Art Unit 2154	Page 1 of 1

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
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	B	US-5,535,365	07-1996	Barriuso et al.	711/155
	C	US-5,701,432	12-1997	Wong et al.	711/130
	D	US-6,487,571	11-2002	Voldman, Jean	708/250
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	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Pai et al. An Evaluation of Memory Consistency Models for Shared-Memory Systems with ILP Processors pages 12-23, 10/96
	V	Grunewald et al. Toward Extremely Fast Context Switching in a Block-Multithreaded Processor 592-599 1996
	W	Bradford et al. Efficient Synchronization for Multithreaded Processors pages 1-4 1/98
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.